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By: GJM:CES:fb1

OSMM&N File No. 211836US2SRD RE

Serial No. 09/915,710

In the matter of the Application of: Yasuo OHBA, et al.

For: SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME

The following has been received in the U.S. Patent Office on the date stamped hereon:

- |   |   |                                 |
|---|---|---------------------------------|
| <input type="checkbox"/> pp. Specification                                  | <input type="checkbox"/> Claims/Drawings                  | <input type="checkbox"/> Sheets |
| <input type="checkbox"/> Combined Declaration, Petition & Power of Attorney | <input type="checkbox"/> Pages                            |                                 |
| <input type="checkbox"/> List of Inventor Names and Addresses               |   |                                 |
| <input type="checkbox"/> Utility Patent Application Transmittal             | <input type="checkbox"/> CPA                              |                                 |
| <input type="checkbox"/> Request for Priority                               | <input type="checkbox"/> Priority Doc                     |                                 |
| <input type="checkbox"/> Check for  | <input checked="" type="checkbox"/> Dep. Acct. Order Form |                                 |
| <input type="checkbox"/> Fee Transmittal Form                               |   |                                 |
| <input type="checkbox"/> Assignment/PTO 1595 pages:                         |   |                                 |
| <input type="checkbox"/> Letter to Official Draftsman                       |   |                                 |
| <input type="checkbox"/> Letter Requesting Approval of Drawing Changes      |   |                                 |
| <input type="checkbox"/> Drawings   | <input type="checkbox"/> sheets                           | <input type="checkbox"/> Formal |
| <input checked="" type="checkbox"/> Cover Letter                            |   |                                 |
| <input checked="" type="checkbox"/> Amendment w/ Marked-up Copy             |   |                                 |
| <input type="checkbox"/> Information Disclosure Statement                   | <input type="checkbox"/> PTO-1449                         |                                 |
| <input type="checkbox"/> Cited References                                   |   |                                 |
| <input type="checkbox"/> Search Report                                      |   |                                 |
| <input type="checkbox"/> Statement of Relevancy                             | <input type="checkbox"/> Cited Pending Applications       |                                 |
| <input type="checkbox"/> IDS/Related/List of Related Cases                  | <input type="checkbox"/> Election Response                |                                 |
| <input type="checkbox"/> Restriction Response                               |   |                                 |
| <input type="checkbox"/> Rule 132 Declaration                               |   |                                 |
| <input type="checkbox"/> Petition for Extension of Time                     |   |                                 |
| <input type="checkbox"/> Notice of Appeal                                   |   |                                 |
| <input type="checkbox"/> Brief  |   |                                 |
| <input type="checkbox"/> Issue Fee Transmittal                              |   |                                 |
| <input type="checkbox"/> White Advance Serial Number Card                   |   |                                 |
| <input type="checkbox"/> Small Entity Status is claimed                     |   |                                 |
| <input type="checkbox"/>  |   |                                 |



Due Date: 8-11-02

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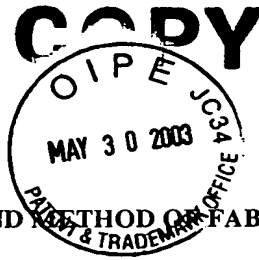
Docket No. 211836US2SRD RE

IN RE APPLICATION OF: Yasuo OHBA, et al.

SERIAL NO: 09/915,710

FILED: July 27, 2001

FOR: SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME



ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

Transmitted herewith is an **Amendment w/ Marked-up Copy** in the above-identified application.

- ☒ No additional fee is required
- ☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
- ☐ Additional documents filed herewith:

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	71	MINUS	71	0	x \$18 =	\$0.00
INDEPENDENT	20	MINUS	20	0	x \$84 =	\$0.00
		<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS			+ \$280 =	\$0.00
		TOTAL OF ABOVE CALCULATIONS				\$0.00
		<input type="checkbox"/> Reduction by 50% for filing by Small Entity				\$0.00
		<input type="checkbox"/> Recordation of Assignment			+ \$40 =	\$0.00
		TOTAL				\$0.00

- ☐ A check in the amount of **\$0.00** is attached.
- ☒ Please charge any additional Fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- ☒ If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

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211836US-2SRD RE

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

YASUO OHBA ET AL

SERIAL NO: 09/915,711

FILED: JULY 27, 2001

FOR: SEMICONDUCTOR DEVICE AND  
METHOD OF FABRICATING THE  
SAME

: EXAMINER: JACKSON, J.

: GROUP ART UNIT: 2815

# 14/B  
7/29/03  
Sunder



AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

Responsive to the Office Action dated March 18, 2002, and the Communication dated July 11, 2002, please amend the above-identified application as follows:

IN THE CLAIMS

Please cancel claims 18-20 and 22 without prejudice.

Please amend the claims as indicated in the marked-up copy to read as follows:

17. (Amended) A semiconductor device comprising:

a substrate;

crystals formed on said substrate and containing at least Al and N, said crystals being

disposed so as to expose portions of said substrate;

a thermal distortion reducing layer made of  $Al_{1-u-v}Ga_uIn_vN$  ( $0 \leq u \leq 1$ ,  $0 \leq v \leq 1$ ,  $u+v \leq 1$ )

formed on said crystals and having a different chemical formula from that of said crystals;

a first cladding layer formed over said thermal distortion reducing layer;

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